WHAT IS CLAIMED, IS:

1. Method for cross interleave Reed-Solomon code correction
   comprising the steps of:
   - inputting a first C1 codeword into C1 decoder means from
     a first memory means;
   - outputting a second C1 codeword into a second memory
     means from the C1 decoder means;
   - de-interleaving the second C1 codeword to produce a
     first C2 codeword in the second memory means.

2. Method according to claim 1, wherein the second C1 code-
   word being the corrected first C1 codeword, in case the
   first C1 codeword is correctable and the second C1 code-
   word being a copy of the first C1 codeword in case the
   first C1 codeword is not correctable.

3. Method according to claim 2, wherein an erasure flag is
   set for each symbol in the second C1 codeword in case
   the first C1 codeword is not correctable.

4. Method according to claim 1, further comprising the
   steps of:
   - inputting the first C2 codeword into C2 decoder means
     from the second memory means;
   - outputting of a second C2 codeword into the second mem-
     ory means from the C2 decoder means.

5. Method according to claim 4, wherein the second C2 code-
   word is the corrected first C2 codeword in case the
   first C2 codeword is correctable, and the second C2
   codeword being a copy of the first C2 codeword in case
   the first C2 codeword is not correctable.

6. Method according to claim 5, further comprising setting
   an erasure flag for each symbol in the second C2 code-

word in case the first C2 codeword is not correctable.

7. Method according to claim 1, further comprising:
- inputting the second C1 codeword into the C1 decoder means from the second memory means;
- outputting a third C1 codeword into third memory means from the C1 decoder means;
- de-interleaving the third C1 codeword to produce a third C2 codeword in the third memory means.

8. Method according to claim 7, wherein the third C1 code-word being the corrected second C1 codeword in case the second C1 codeword is correctable, and the third C1 codeword being a copy of the second C1 codeword in case the second C1 codeword being not correctable.

9. Method according to claim 8, further comprising setting an erasure flag for each symbol in the third C1 codeword in case the second C1 codeword is not correctable.

10. Method according to claim 7, further comprising:
- inputting the third C2 codeword into the C2 decoder from the third memory means;
- outputting a fourth C2 codeword into a fourth memory means from the C2 decoder means.

11. Method according to claim 10, wherein the fourth C2 codeword being the corrected third C2 codeword in case the third C2 codeword is correctable, and the fourth C2 codeword being a copy of the third C2 codeword in case the third C2 codeword is not correctable.

12. Method according to claim 11, further comprising setting an erasure flag for each symbol in the fourth C2 code-word in case the third C2 codeword is not correctable.

13. Computer program product comprising a program performing a method in accordance with claim 1.

14. Reed-Solomon decoder carrying out cross interleave Reed-Solomon code correction in accordance with a method of claim 1.

15. Audio or video device, for example a CD or DVD player or recorder, comprising a Reed-Solomon decoder in accordance with claim 14.